

U.S.S.N. 09/961,049

SPECIFICATION:

Please amend the specification as follows:

On page 11, please replace the amended paragraph beginning at line 19 with the following paragraph:

Referring now to FIG. 12, illustrated therein is a schematic block diagram in accordance with the present invention. The circuit 200 includes a capacitor 201 in the cable 203. The capacitor 201 has a value that corresponds to a specific electronic device (not shown). The capacitor 201 is coupled in series with a resistor 201 to ground. While the capacitor 201 is disposed in the cable 203, the resistor 202 201 is disposed in the universal device 204. When the cable 203 is attached to the universal device 204, the microprocessor 209 of the universal device 204 takes the capacitor connection 205 and applies a step function 210 by toggling an output 206 from low to high. When this occurs, an exponentially decaying, or "shark-fin", function 211 is created across the resistor 202.